

PTO/SB/00A (03-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 8

**Complete if Known**

Application Number	10/828,495
Filing Date	April 20, 2004
First Named Inventor	Julian Partridge, et al.
Art Unit	2822
Examiner Name	Thanh Y Tran
Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
T		US-6,919,626 B2	7/19/2005	Burns	
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Sheet	2	of	8	Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763

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Substitute for form 1449/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)				Application Number	10/828,495
				Filing Date	April 20, 2004
				First Named Inventor	Julian Partridge, et al.
				Art Unit	2822
				Examiner Name	Thanh Y Tran
Sheet	7	of	8	Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
TYT		US-2005/0040508 A1	2/24/2005	Lee	
		US-2005/0035440 A1	2/17/2005	Mohammed	
		US-2004/0245617 A1	12/9/2004	Damberg, et al.	
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		US-2004/0203190 A1	10/14/2004	Pflughaupt, et al.	
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		US-6,707,684 B1	3/16/2004	Andric, et al.	
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		US-6,360,433 B1	3/26/2003	Ross	
		US-6,336,262	1/8/2002	Dalal, et al.	
		US-6,234,820 B1	5/22/2001	Perino, et al.	
		US-6,222,737 B1	4/24/2001	Ross	
		US-6,040,624	3/21/2000	Chambers, et al.	
		US-6,034,878	3/7/2000	Osaka, et al.	
		US-6,002,589	12/14/1999	Perino, et al.	

FOREIGN PATENT DOCUMENTS						
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

Examiner Signature	Thanh Tran	Date Considered	12/23/05
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

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Signature**

Thank Tran

Date  
Considered

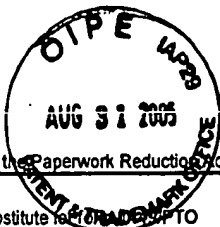
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Substitute for PTO		<b>Complete if Known</b>	
		Application Number	10/828,495
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		Filing Date	April 20, 2004
		First Named Inventor	Julian Partridge, et al.
		Art Unit	2822
		Examiner Name	Thanh Y Tran
		Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763
Sheet	1	of	3

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		WO 03/037053 A1	5/1/2003	Cady, et al.		
		JP 63-153849 (A)	6/27/1998	Matsubara		
		JP 62-230027 (A)	8/10/1987	Nakamura		
		JP 4-209562 (A)	7/30/1992	Uzuka, et al.		✓
		JP 404368167 (A)	12/21/1992	Omura		
		JP 2003/347503 (A)	12/5/2003	Toshiyuki, et al.		✓

Examiner Signature	Thanh Tran	Date Considered	12/23/05
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Substitute for form 1449/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		Application Number	10/828,495
		Filing Date	April 20, 2004
		First Named Inventor	Julian Partridge, et al.
		Art Unit	2822
		Examiner Name	Thanh Y Tran
Sheet 2 of 3	Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763	

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Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
		JP 2003/347475 (A)	12/5/2003	Mitsuru		
		JP 2003/309247 (A)	10/31/2003	Mitsuru		
		JP 2000/307029 (A)	11/2/2000	Mitsuru		
		JP 60-254762 (A)	12/16/1985	Fujitsu		
		JP 50-29534 (A)	2/5/1993	Nakamura		
		JP 3641047659 (A)	3/8/1986	Soga, et al.		

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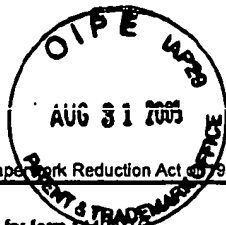
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Substitute for form 1449-0102		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		Application Number	10/828,495
		Filing Date	April 20, 2004
		First Named Inventor	Julian Partridge, et al.
		Art Unit	2822
		Examiner Name	Thanh Y Tran
		Attorney Docket Number	254-094-CIP4/CIP-MB / AK 153763
Sheet	1	of	2

NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
TYT		Flexible Printed Circuit Technology - A Versatile Interconnection Option. (Website 2 pages) Fjelstad, Joseph. December 3, 2002.	
		Chip Scale Packaging and Redistribution, Paul A. Magill, Glenn A. Rinne, J. Daniel Mis, Wayne C. Machon, Joseph W. Baggs, Unitive Electronics Inc.	
		Dense-Pac Microsystems, 16 Megabit High Speed CMOS SRAM DPS1MX16MKn3	
		Dense-Pac Microsystems, 256 Megabyte CMOS DRAM DP3ED32MS72RW5	
		Dense-Pac Microsystems, Breaking Space Barriers, 3-D Technology 1993	
		Dense-Pac Microsystems, DPS512X16A3, Ceramic 512K X 16 CMOS SRAM Module	
		IBM Preliminary 168 Pin SDRAM Registered DIMM Functional Description & Timing Diagrams	
		3D Interconnection for Ultra-Dense Multichip Modules, Christian VAL, Thomson-CSF DCS Computer Division, Thierry Lemoine, Thomson-CSF RCM Radar Countermeasures Division	
		High Density Memory Packaging Technology High Speed Imaging Applications, Dean Frew, Texas Instruments Incorporated	
		Vertically-Integrated Package, Alvin Weinberg, Pacesetter, Inc. and W. Kinzy Jones, Florida International University	

Examiner Signature	Thanh Tran	Date Considered	12/23/05
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		Filing Date	April 20, 2004
		First Named Inventor	Julian Partridge, et al.
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TYT		1992 Proceedings, 42nd Electronic Components & Technology Conference, May 18-20, 1992	
		Research Disclosure, Organic Card Device Carrier, 31318, May 1990, No. 313	
		IBM Technical Disclosure Bulletin, Vol. 23, No. 12, May 1981	
		IBM Technical Disclosure Bulletin, Vol. 20, No. 11A, Apr. 1978	
		IBM Technical Disclosure Bulletin, vol. 32, No. 3B, Aug. 1989	
		Orthogonal Chip Mount - A 3D Hybrid Wafer Scale Integration Technology, International Electron Device Meeting, IEDM Technical Digest, Washington, D.C., Dec. 6-9, 1987	

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